**North South University**

Department of Computer Science and Engineering

**Quiz-4**, Section – 5, Fall-2016

Course No: **CSE 231** Course Title: **Digital Logic Design**

Time: 10 min Full Marks:10

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|  |  |  |
| 1. | Build a 4 bit magnitude comparator using 4 blocks of 1bit magnitude comparator. | 10 |
|  | A==B  A<B  A>B  A0  B0  1 bit Magnitude comparator |  |